

# 淡江大學 102 學年度日間部轉學生招生考試試題

系別：電機工程學系三年級

科目：電子學

考試日期：7月24日(星期三) 第1節

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本試題雙面印刷

1. As depicted in Fig. 1, (a) Compute the  $R_{in}$ ,  $R_{out}$  and voltage gain of Common-Emitter for  $V_A = \infty$ . (b) Compute the  $R_{in}$  and  $R_{out}$  of Common-Emitter for  $V_A \neq \infty$ . (20%)

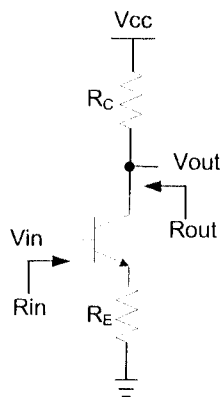


Fig.1

2. As depicted in Fig. 2, (a) Compute the  $R_{in}$ ,  $R_{out}$  and voltage gain of Common-Base for  $V_A = \infty$ . (b) Compute the  $R_{in}$  and  $R_{out}$  of Common-Base for  $V_A \neq \infty$ . (20%)

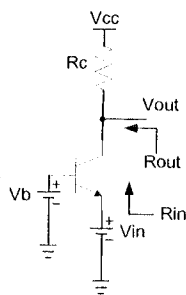


Fig. 2

3. If the collector and base of a bipolar transistor are tied together, a two-terminal device results. Determine the small-signal impedance of the devices shown in Fig. 3. Assume  $V_A = \infty$ . (20%)

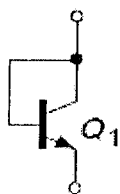


Fig.3

4. As shown in Fig. 4, (a) Compute the  $R_{in}$ ,  $R_{out}$  and voltage gain of Common-Emitter by using small signal model under  $V_A = \infty$ . (b) Compute the  $R_{in}$ ,  $R_{out}$  and voltage gain of Common-Emitter by using small signal model under  $V_A < \infty$ . (20%)

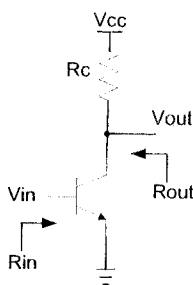


Fig.4

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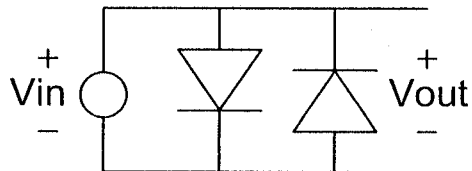
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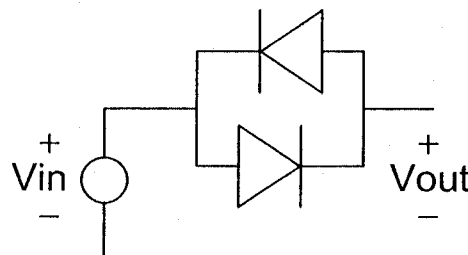
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5. Plot the output/input characteristics of the circuits depicted in Fig. 5 (a) and (b). Assume  $V_{D_{on}} = 0.8V$ . (20%)



(a)



(b)